



NATEA SIG Wireless Seminar

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Date/Time: Monday, December 6, 2004 / 6:00 - 8:30 PM
Place: Mountain View City Library (free basement parking, see the map below)
585 Franklin St., Mountain View, CA 94041

Subject 1: **The Path Toward Analog & RF Integration**

Speaker: **Paul Kempf, CTO & CMO, Jazz Semiconductor**

Abstract

While a segment of the industry has been emphasizing a trend toward CMOS integration for precision analog and RF circuitry on a system-level chip, a different reality is evident when you take a look at what's inside mainstream products: a number of chips made with different technologies that meet real world requirements. Even the products shipped by the most celebrated CMOS-only companies are typically made up of multiple die utilizing different technology generations in order to meet voltage and isolation needs, as well as balance cost versus performance for the sub-system components.

The industry roadmap does not provide a technology path for single chip integration of all analog blocks in advanced CMOS, so while the digital sub-system absorbs the signal processing requirements for new networking and multi-media functions, integration of the analog sub-system stands as an opportunity that is realizable in a silicon technology optimized to meet the analog and RF requirements at the best cost versus performance trade-off.

This talk explores the process technology requirements and provides product development examples on the path toward analog sub-system integration, while discussing the emergence of a new analog foundry model in support of this trend.

Speaker Biography

Paul Kempf is currently the Chief Marketing and Technology Officer at Jazz Semiconductor, with responsibilities for Engineering, Marketing, Quality and China Operations. Before joining Jazz Semiconductor in March 2002, Mr. Kempf was the Vice President of the Silicon RF Platform group at Conexant Systems, where he led the development of advanced analog and RF CMOS processes and was responsible for establishing SiGe BiCMOS technologies for wireless and optical networking applications. Mr. Kempf held several management positions with Rockwell Semiconductor Systems prior to the Conexant spin-off in January 1999. Prior to joining Rockwell, Mr. Kempf spent 11 years with Nortel Ltd. in a variety of positions that included silicon device design, process integration and program management. Mr. Kempf holds bachelor's and master's degrees in engineering physics from McMaster University in Canada.



Subject 2: The Global Fabless Model

Speaker: Dr. Jeremy Wang, Asia-Pacific Executive Director, Fabless Semiconductor Association (FSA)

Abstract

As it is the 10th anniversary of Fabless Semiconductor Association (FSA), the presentation will show the superiority of the fabless business model, which has demonstrated strong growth and impacts for the semiconductor industry in the past decade. At the same period of time, the global semiconductor environment has been evolving dramatically and some recent up-rising developments in Asia Pacific like Taiwan, Korea and China will be discussed. The challenges of future successful innovation model will conclude the talk.

Speaker Biography

Dr. Jeremy Wang is Asia-Pacific executive director for the FSA. In this role, Dr. Wang oversees all aspects of managing the FSA’s presence in the region. Dr. Wang is responsible for working with key executives in Asia to encourage their membership and participation with the FSA, promoting a unified and global voice for the semiconductor industry. He also manages the Asia-Pacific Leadership Council, the group responsible for determining the direction and organization of the FSA’s Asian presence. Dr. Wang works closely with the U.S. headquarters of the FSA to bring its popular events to the region, including the Association’s many educational luncheons and one of the premier semiconductor industry events, the FSA Suppliers Expo.

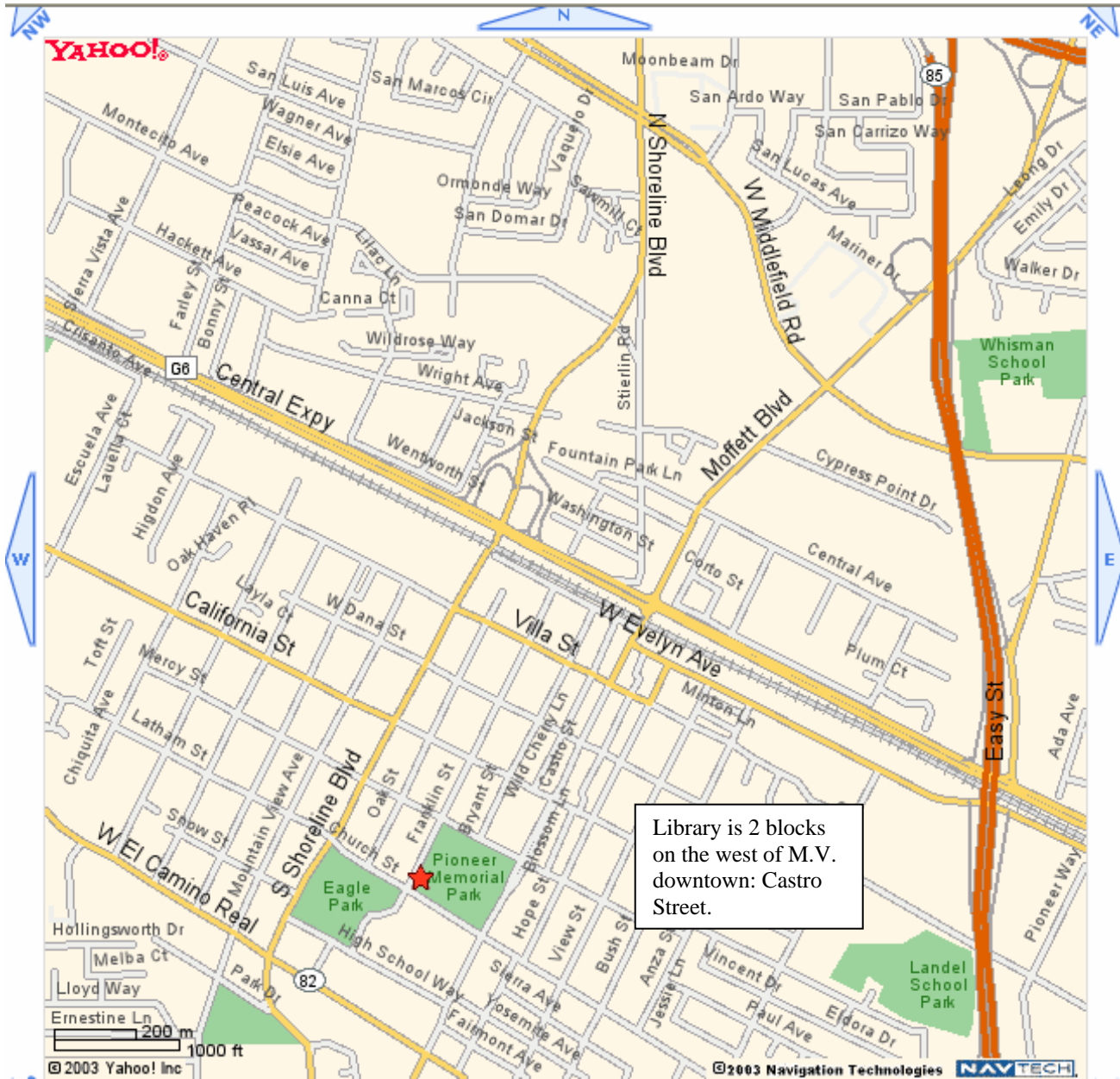
Prior to joining the FSA, Dr. Wang held various executive-level positions at a leading Asian venture capital firm responsible for fund management in the U.S., Taiwan and China, with investment portfolios in semiconductor, IC design, software, and advanced materials.

Dr. Wang also served as country manager for CFM Technologies, a semiconductor manufacturing equipment company acquired by Mattson Technology, where he was responsible for establishing and managing the company’s Taiwan office. Previously, he was a research engineer at Winbond Electronics.

Dr. Wang earned his Ph.D. in Materials Science from University of Wisconsin-Madison and a bachelor of science in Physics from National Tsing-Hua University in Taiwan. He has published more than 10 research papers and holds two patents in semiconductor process technologies



(See below for the map)



Library is 2 blocks on the west of M.V. downtown: Castro Street.

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