



NATEA SIG Wireless Seminar

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Date/Time: Tuesday, May 18, 2004 / 6:00 - 8:00 PM (6-6:30 networking)
Place: Mountain View City Library (free basement parking, see the map below)
585 Franklin St., Mountain View, CA 94041

**Subject: Process Design Kit (PDK) Standard of Mixed-Signal/RF IC Industry
by FSA**

Speaker: Mr. Ken Brock, VP of Marketing, Silvaco International

Abstract

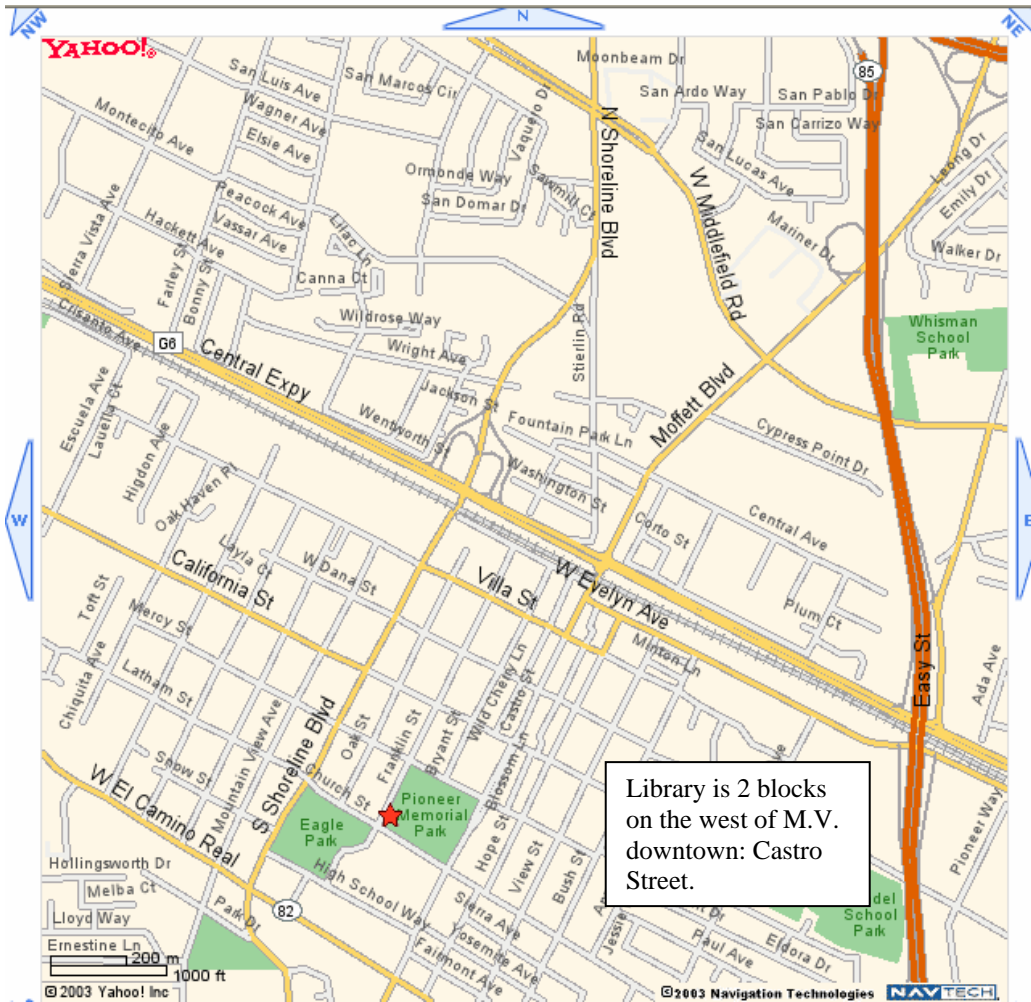
Just as there are lot-to-lot, wafer-to-wafer, and die-to-die variances in any MS/RF silicon process, there are significant foundry-to-foundry, process-to-process, and EDA vendor-to-vendor differences in the quality of process design kit (PDK)s. The Fabless Semiconductor Association's Mixed-Signal/RF Foundry Committee's PDK working group has just released its MS/RF PDK Checklist. The Checklist is a two-page, FSA-endorsed document completed by the PDK developer and will be delivered with each new release of a mixed-Signal/RF PDK. It contains relevant contact information, foundry source document references, EDA tool versions and a device list with the deliverables and tests performed for each device. It serves as a "nutrition facts label" for a MS/RF PDK.

In this tutorial, the chairman of this committee will demonstrate how this checklist will help RF designers gain a better understanding of the source data, quality and risk in using a PDK to design RF ICs.

Speaker Biography

Ken Brock is chairman of the FSA Mixed-Mode/RF Foundry Committee PDK working group and VP of Marketing for Silvaco International. He has been involved with the FSA since its founding when it absorbed the Foundry Users Network that became the core of the FSA Technical Committee. Prior to joining Silvaco, he has held marketing, consulting, customer service, and product development positions at Virtual Silicon, Collett International, Compass Design, Mentor Graphics, Silicon Compilers, HHB-Softron, Vydec and Dataprobe. He holds a BSEE and MBA from Fairleigh Dickinson University.





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